



Gabara 91-2-36

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): T.J. Gabara et al.
Case: 91-2-36
Serial No.: 10/719,193
Filing Date: November 21, 2003
Group: 2816
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Vulpis Date: March 1, 2004

Title: Integrated Circuit with Controllable Test Access
to Internal Analog Signal Pads of an Area Array

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please issue a corrected filing receipt correcting the third inventor's last name, by changing "Mrris" to "Morris". A marked-up copy of the filing receipt and a copy of page 4 of the Declaration are enclosed.

Respectfully submitted,

Date: March 1, 2004

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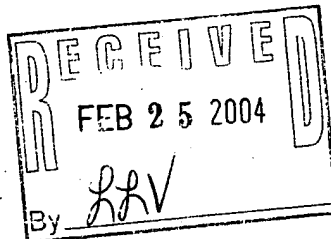


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/719,193	11/21/2003	2816	770	91-2-36	6	20	3

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CONFIRMATION NO. 2999

FILING RECEIPT



OC000000011949768

Date Mailed: 02/23/2004

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Applicant(s)

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Morris

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/23/2004

Projected Publication Date: 05/26/2005

Non-Publication Request: No

Early Publication Request: No

Title

Integrated circuit with controllable test access to internal analog signal pads of an area array

Preliminary Class

327

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